

BUGBASE 2.0 DOCK BOARD

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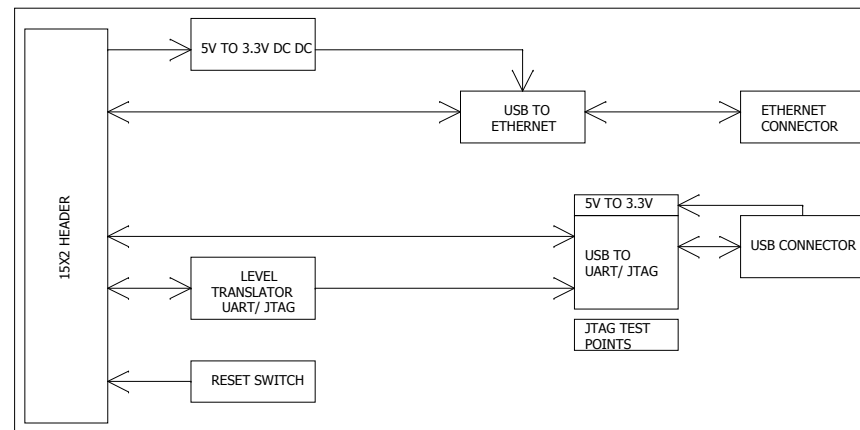
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REV	B
VERSION	2.00

VERSION HISTORY

VER #	DATE	ACTION	DESCRIPTION OF CHANGES	AUTHOR	REVIEWED BY	APPROVED BY
0.10	23 JULY 2009	CREATED	Initial draft.	PRASAD ACHAR	AVINASH B	AVINASH B
0.20	14 AUG 2009	Review Comments Updated	USB HOST connector added	PRASAD ACHAR	AVINASH B	AVINASH B
1.00	14 AUG 2009		Baseline Version	PRASAD ACHAR	AVINASH B	AVINASH B
1.10	24 SEPT 2009	Schematics updated	UART to USB Change updated	PRASAD ACHAR	AVINASH B	AVINASH B
1.20	19 FEB 2010	Schematics updated	FTDI in self power mode Dock Reset pull up added FTDI 3V3OUT used to power level translator and JTAG FTDI reset left floating DOCK Connector changed to male right angle type. Added Test Points for power pins Added DNI property to complete RS232 section.	AJIT M B	AVINASH B	AVINASH B
1.21	9 MAR 2010	Schematics updated	1.8V LDO Added. FTDI IO powered by 1.8V. Dock Connector changed to 15x2 Header JTAG section made DNI	AJIT M B	AVINASH B	AVINASH B
1.22	7 APR 2010	Schematics updated	Dock Connector changed to 15x2, 1mm pitch Header Added FT232D chipset, The HOST USB connector is changed to stacked connector 3V3 LDO added for FTDI IO power, 1V8 LDO removed and connected to the VIO_1V8	PRASAD ACHAR	AVINASH B	AVINASH B
	16 APR 2010	Schematics updated	Dock connector changed to a single 30x1 1.27mm pitch header. Stacked USB connector removed to retain individual connectors. JTAG EMU0 & EMU1 pull resistors added. De-caps added for JTAG level translator and SPI EEPROM. Reset switch changed to KSC341J LFS. 2mm mounting holes added.	HARSHITH		
2.00	04 MAY 2010	Baselined	Schematics Back-Annotated and baselined.	HARSHITH	AVINASH B	AVINASH B

REV NO.	NATURE OF CHANGE	APPROVED BY	DATE	 Mistral Solutions [P] Ltd. #60 Adarsh regent,100 Feet Ring Road, Domlur Extension Bangalore 560 071, Ph : +91-80-30912600, Fax :+91-80-2535 6444		Title VERSION HISTORY			
				Drawn by: HARSHITH Date: Tuesday, June 16, 2009	Approved by: Avinash Date: Wednesday, May 05, 2010	Size C	Document Number MS_BUGLABS_BUGBASE_2.0_DOCK_SCH	Rev 2.00	
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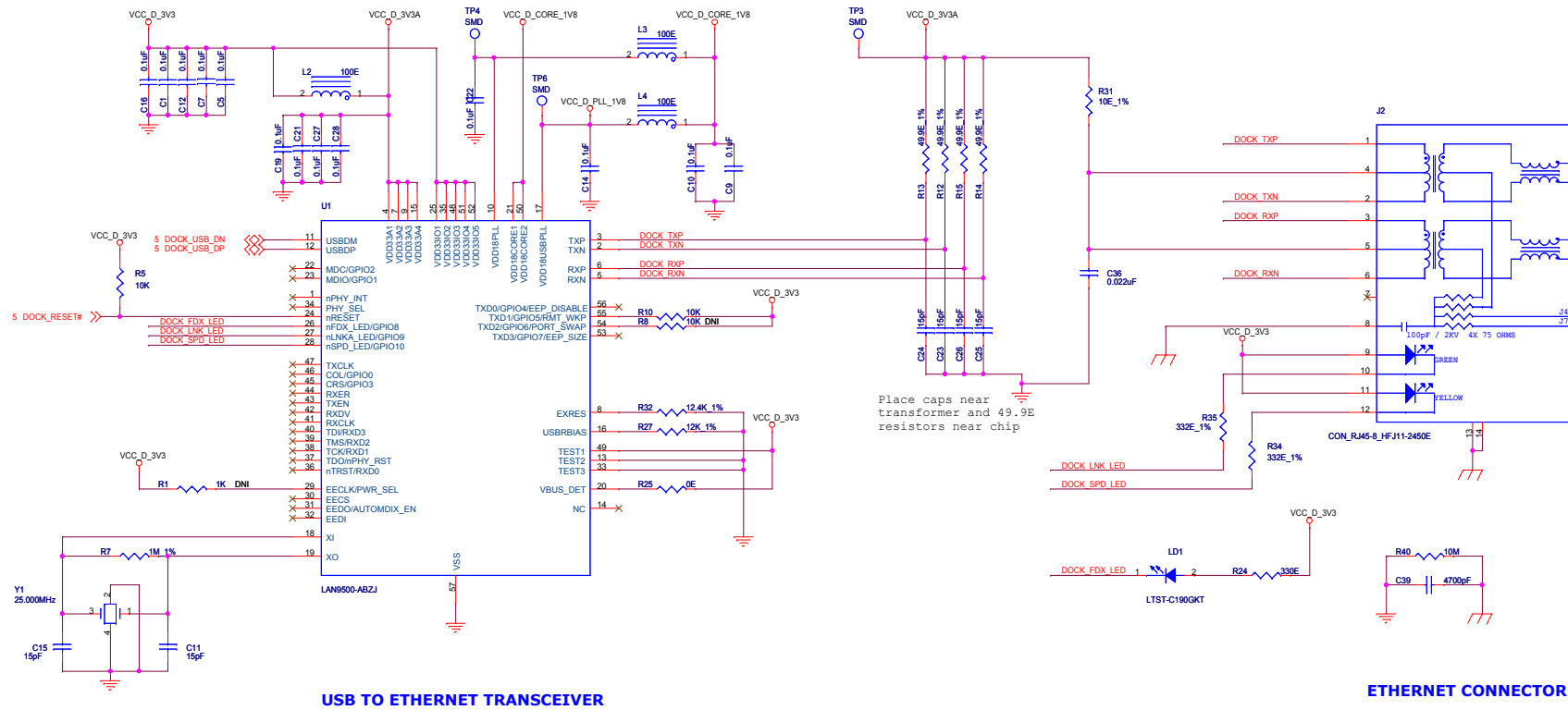
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Date:	Tuesday, June 16, 2009
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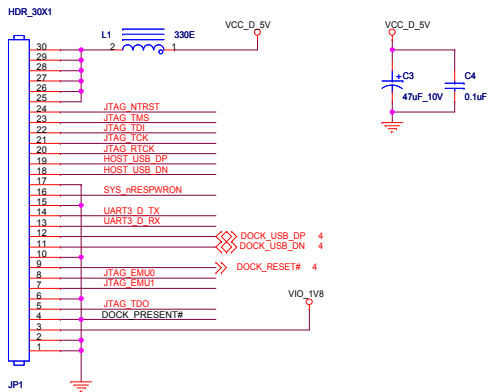
Approved by:	Avinash
Date	Wednesday, May 05, 2010

Title **BLOCK DIAGRAM**

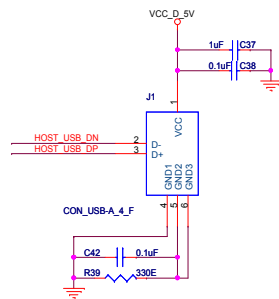
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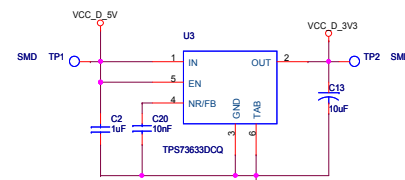
REV NO.	NATURE OF CHANGE	APPROVED BY	DATE	Mistral Solutions [P] Ltd. #60 Adarsh regent, 100 Feet Ring Road, Domlur Extension Bangalore 560 071, Ph : +91-80-30912600, Fax : +91-80-2535 6444		USB TO ETHERNET Document Number MS_BUGLABS_BUGBASE_2.0_DOCK_SCH	
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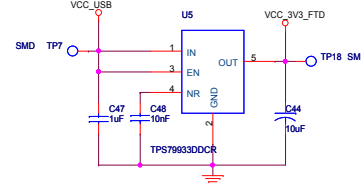
DOCK CONNECTOR



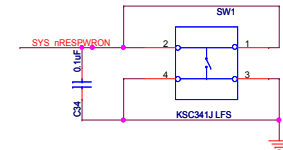
USB HOST CONNECTOR



**5V TO 3.3V LDO
USB - ETHERNET**

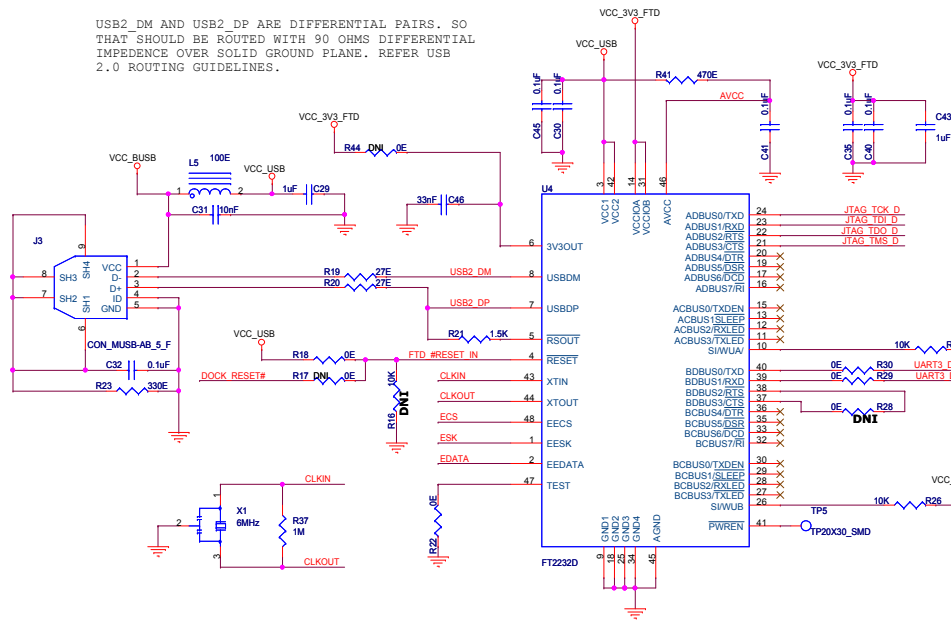


**5V TO 3.3V LDO
DUAL UART - USB FTDI CHIP**

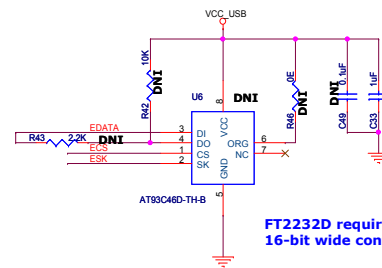


RESET SWITCH

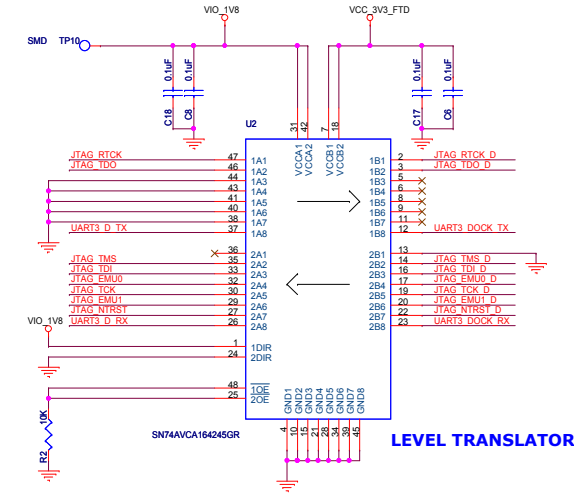
USB2_DM AND USB2_DP ARE DIFFERENTIAL PAIRS. SO THAT SHOULD BE ROUTED WITH 90 OHMS DIFFERENTIAL IMPEDANCE OVER SOLID GROUND PLANE. REFER USB 2.0 ROUTING GUIDELINES.



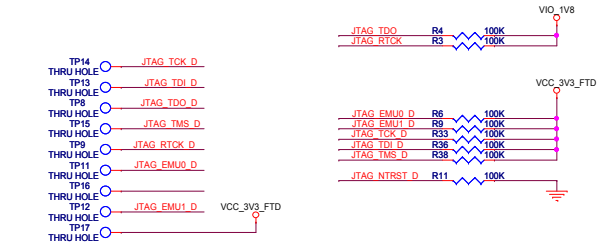
UART AND JTAG TO USB CONVERTER



**FT232RL requires
16-bit wide configuration.**



LEVEL TRANSLATOR





2mm Mounting Holes

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