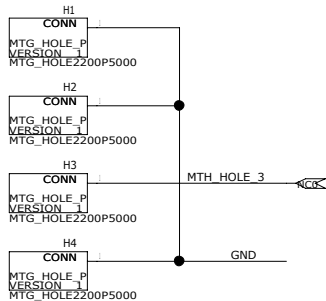
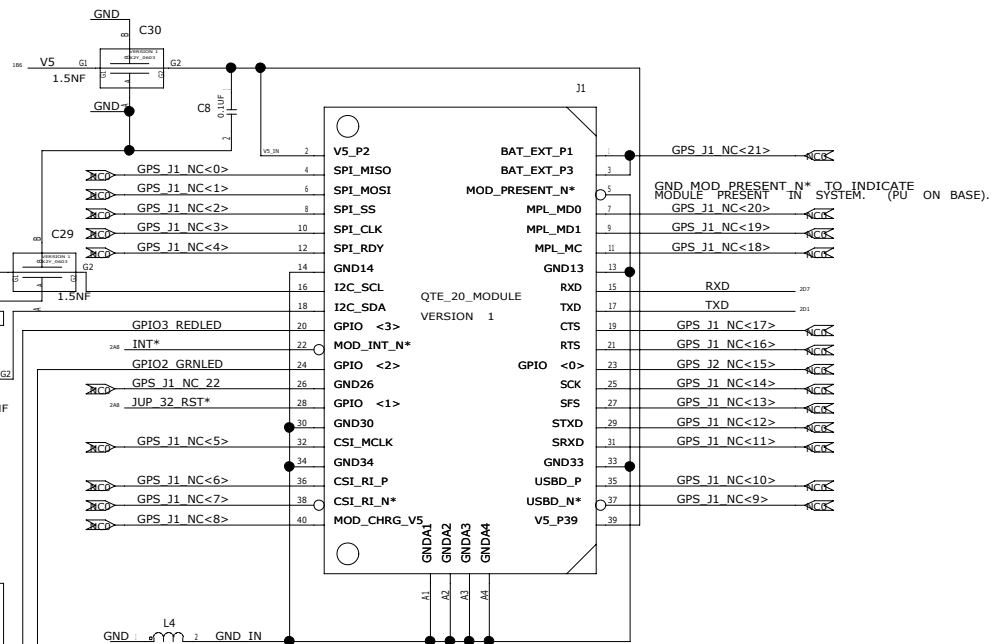
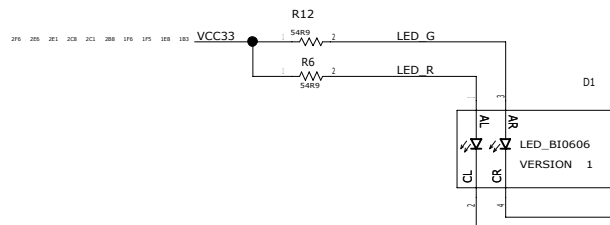
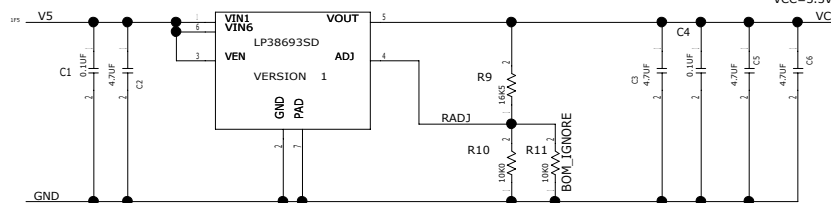


I2C ADDRESS OF EEPROM IS 0XA0

INSTALL R5 IN PRODUCTION TO
ALLOW U1 TO BE PROGRAMMED.
REMOVE R5 TO WRITE PROTECT U1.



ADJ 500MA LDO
INPUT & OUTPUT CAPS 1.0-10UF
U3

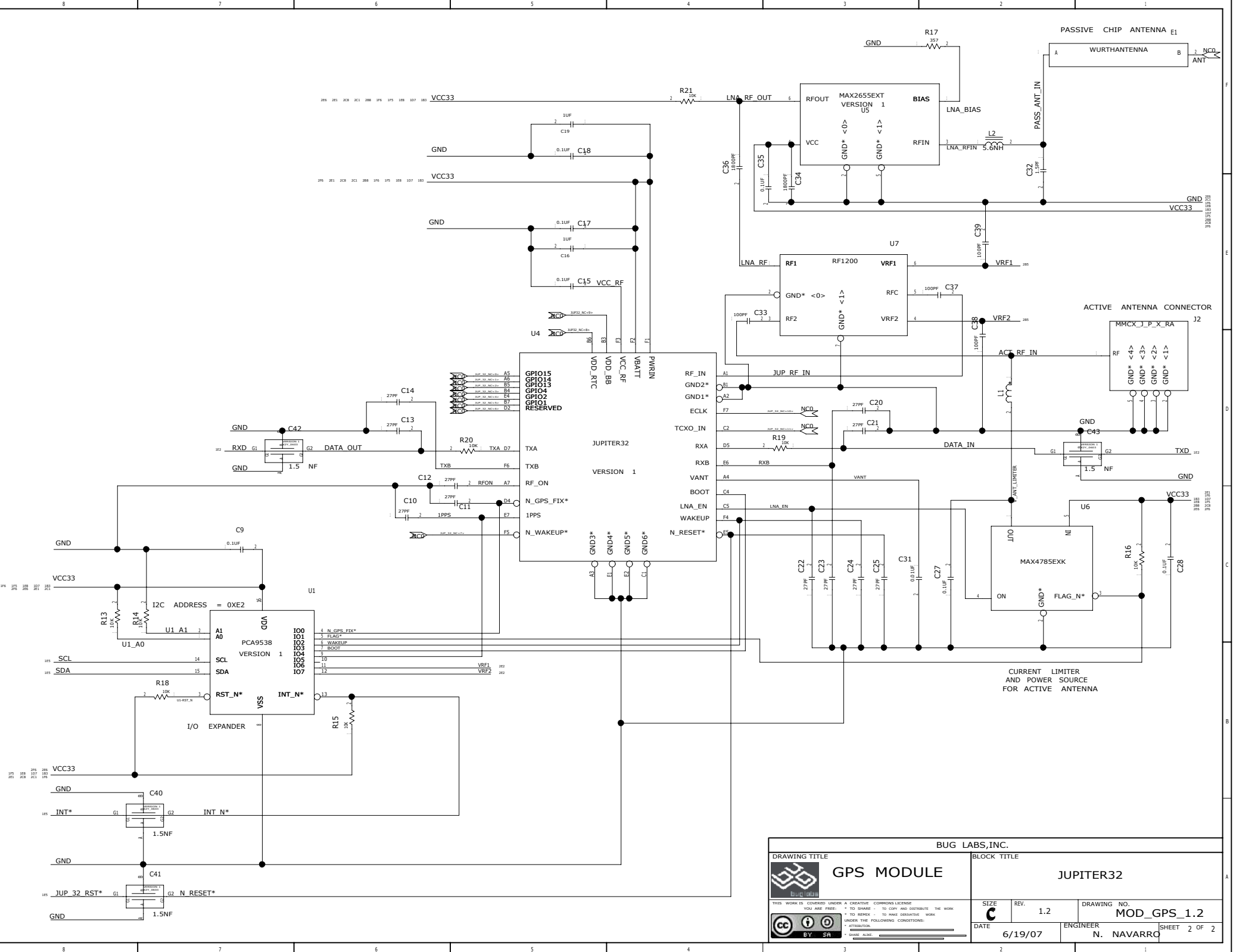


VOUT=1.25V(1+(RUP/RDN))
VOUT=3.31V WITH RUP=16K5 RDN=10K0

SECOND ADJ RESISTOR SITE R11
PROVIDED FOR TWEAKING

PLACE C8 & C19 AWAY FROM LDO AS
GENERAL BOARD BULK BYPASS, IF NEEDED.

DRAWING TITLE		BLOCK TITLE	
MODULE:		BASE CONNECTOR, EEPROM, AND LDO	
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DATE 6/19/07		DRAWING NO. MOD_GPS_1.2	ENGINEER N. NAVARRO
SHEET 1 OF 2			



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 GPS MODULE		BLOCK TITLE	
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DATE 6/19/07		DRAWING NO. MOD_GPS_1.2	
ENGINEER N. NAVARRO		SHEET 2 OF 2	